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CONFIRMATION NO. 9254

SERIAL NUMBER 10/539,103	FILING OR 371(c) DATE 06/15/2005 RULE	CLASS 438	GROUP ART UNIT 2823	ATTORNEY DOCKET NO. NL02 1448 US	
APPLICANTS Paul Leon Cecile Simon, Revel, FRANCE; Aalt Marco Van De Pol, Nijmegen, NETHERLANDS;					
** CONTINUING DATA ***** This application is a 371 of PCT/IB03/05763 12/05/2003 <i>YES</i>					
** FOREIGN APPLICATIONS ***** EUROPEAN PATENT OFFICE (EPO) 02080535.4 12/20/2002 <i>YES</i>					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/15/2006					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged		STATE OR COUNTRY FRANCE	SHEETS DRAWING 5	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 2
ADDRESS 24738					
TITLE Method of producing semiconductor elements using a test structure					
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		